

Sub AS7

ABSTRACT OF THE DISCLOSURE

Provided is an apparatus for detecting a correlation of samples with a spread code comprising: an L-chip accumulator which inputs the samples to generate and output an intermediate correlation signal; memories as many as
5 M, each of which stores samples of the intermediate correlation signal as many as $L \times N$; an adder which has input terminals as many as M and inputs from each of the input terminals the intermediate correlation signal which is outputted from the L-chip accumulator or the intermediate correlation signal which is outputted from a corresponding memory among the memories; and a
10 controller which supplies the intermediate correlation signal outputted from the L-chip accumulator to the memories as many as M and to the input terminals as many as M of the adder in rotation with a unit of $L \times N$ samples, and reads, and supplies to each of the input terminals of the adder, the intermediate correlation signal which has been stored in each of the memories
15 M-1 times; wherein an output of the adder is outputted as a correlation signal outputted from the apparatus.